

```

IP:      0x4 0x5 fSkip(152)    fReturn(IPOPTIONS0);
IP:      0x4 0x6 fSkip(184)    fReturn(IPOPTIONS1);
IP:      0x4 0x7 fSkip(216)    fReturn(IPOPTIONS2);
IP:      0x4 BITS:4           fReturn(IPUNKNOWN);

```

(a)

```

IP:      0x4 IPHDR;
IPHDR:   0x5 fSkip(152)        fReturn(IPOPTIONS0);
IPHDR:   0x6 fSkip(184)        fReturn(IPOPTIONS1);
IPHDR:   0x7 fSkip(216)        fReturn(IPOPTIONS2);
IPHDR:   BITS:4               fReturn(IPUNKNOWN);

```

(b)

**Figure 20.5** (a) An example FPL program in which a set of patterns shares a common initial sequence, and (b) an equivalent program with the common sequence folded. A compiler performs the folding automatically.