

Address	Contents
FFFF FFFF	<b>SDRAM Bus:</b> SDRAM Scratchpad FBI CSRs Microengine xfer Microengine CSRs
C000 0000	
B000 0000	AMBA xfer
A000 0000	Reserved
9000 0000	System regs
8000 0000	Reserved
4000 0000	<b>PCI Bus:</b> PCI memory PCI I/O PCI config Local PCI config
0000 0000	<b>SRAM Bus:</b> SlowPort SRAM CSRs Push/Pop cmd Locks BootROM