

| Stage | Description |
|--------------|---|
| 1 | Fetch the next instruction |
| 2 | Decode the instruction and get register address(es) |
| 3 | Extract the operands from registers |
| 4 | Perform ALU, shift, or compare operations and set the condition codes |
| 5 | Write the results to the destination register |

Figure 20.3 The five execution stages uses in a microengine on the IXP1200.