

	absolute addr.	used by	relative addr.
A bank (64 regs.)	48 - 63	context 3 (16 regs.)	0 - 15
	32 - 47	context 2 (16 regs.)	0 - 15
	16 - 31	context 1 (16 regs.)	0 - 15
	0 - 15	context 0 (16 regs.)	0 - 15
B bank (64 regs.)	48 - 63	context 3 (16 regs.)	0 - 15
	32 - 47	context 2 (16 regs.)	0 - 15
	16 - 31	context 1 (16 regs.)	0 - 15
	0 - 15	context 0 (16 regs.)	0 - 15

Figure 20.7 Absolute and relative addresses used for the general-purpose registers on an Intel microengine. In relative addressing mode, half of the registers assigned to a context are taken from each bank.