

<b>Operation</b>	<b>Meaning</b>
<b>set_bits</b>	<b>Set the specified bits to one</b>
<b>clear_bits</b>	<b>Set the specified bits to zero</b>
<b>test_and_set_bits</b>	<b>Place the original word in the read transfer register, and set the specified bits to one</b>
<b>test_and_clear_bits</b>	<b>Place the original word in the read transfer register, and set the specified bits to zero</b>

**Figure 25.1** Possible values for the fifth operand in a bit manipulation operation. Each operation is guaranteed to be atomic.