

**Port card**

An Agere APP550 with associated memory and a connection to the HDS bus over which the APP550 can access I/O ports.

**I/O card**

Facilities for packet input and output: four 1-Gbps optical Ethernet connections and an OC-48 TADM connection that can be configured as an OC-48C connection or a mixture of up to four OC-12 ATM and/or Packet Over SONET connections.

**CPU card**

A PowerPC processor used to control the APP550, ROM and PROM memories, a serial port, an Ethernet connection, and a debugging port.

**Figure 19.2** The three boards in Agere's Hardware Development System and the contents of each. The HDS serves as a testbed that allows engineers to build and evaluate software for the Agere network processor.