SRAM access unit
SRAM
clock
signals
address
data
buffer
Flash (Boot ROM)
Memory Mapped I/O
SRAM pin inter-face
data
addr
service priority arbitration
memory & FIFO
AMBA bus inter-face
AMBA
from StrongARM
AMBA addr. queues
command decoder & addr. generator
microengine addr. & command queues
Microengine commands
microengine data