optional host connection
PCI bus
SRAM bus
IXP1200 chip
SRAM
SRAM access
PCI access
Embedded RISC processor (StrongARM)
serial line
FLASH
multiple, independent internal buses
Microengine 1
scratch memory
Microengine 2
Memory Mapped I/O
Microengine 3
Microengine 4
Microengine 5
SDRAM
SDRAM access
IX access
Microengine 6
SDRAM bus
High-speed I/O bus
IX bus